Curriculum Vitae

Hassan Saleh, Assistant Lecturer.
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Work Address:

Atomic Energy Authority (AEA),

National Center for Radiation Research and Technology (NCRRT),

Engineering Dept.,

3Ahmed Elzomor st., Nasr City, P. O. Box 29, Cairo

Phone: +202738665
Email: h_i_saleh@hotmail.com.com

Home Address:

27 Rhoda St., Abu-Elnumrus City, Giza

Phone: +20-280-93823
Birth date: March 21, 1970
Citizenship: Egypt
Martial Status: Married

A. Academic Degrees

1. PhD

Major: Electronic Circuits
Minor: Digital Circuits
Degree: PhD.,
Honors: Cairo university 1999- March 2003
Thesis:

"High Performance Realizations of DSP algorithms"

2. M. Sc.

Major: Electronic Circuits
Minor: Digital Circuits
Degree: M. Sc., 1998 (Distinction)
Honors: Cairo university 1996-1998
Honors Thesis:

"FPGA-based Design and Implementation of Serial-parallel Multipliers with PCI Interface."

3. B. Sc.

Major: Electronics and Electrical Communications
Degree: B. Sc., (Very Good with honors 82%)
Honors: Cairo university 1987-1992

C. Professional Positions

1. **1998- present, Assistant Lecturer,** Atomic energy Authority,
   NCRRT, Nasr city, Cairo.
   - Teaching VHDL and circuits design concepts courses for Engineers and students' summer-trainings.
   - Searching in Digital Circuits, Electronic Circuits, FPGA, DSP, Arithmetic Logic, Nuclear Engineering, etc.
   - Instruction in courses for both undergraduate and graduate training programs.
2. **1995- 1998 Administrator**, Atomic energy Authority,
   NCRRT, Nasr city, Cairo.
3. **2002-pres, Senior Designer**, Microelectronics Design Center, Atomic energy Authority, , Nasr city, Cairo.
   Supervising the requested projects and designs as a third party center of different partners.

D. Academic Cooperation

Aug.1996- Aug.1997. Research Visitor, **Zentral Electronik Labor (ZEL), ForschungsZentrum Juelich (FZJ), Juelich, Germany.**

- Training and working on research and design of the projects of ZEL such as PCI interfaces cards, GUI, Labview.

Aug.2000-Dec.2001. Research Visitor, **Zentral Electronik Labor (ZEL), ForschungsZentrum Juelich (FZJ), Juelich, Germany.**

- Cooperating in research and design of the projects of ZEL such as, PET scanners, Noise Thermometer, and Pulse Shape Discriminator.

E. Membership in Professional Associations

Association for Computing Machinery (ACM)

(Student Member)

F. Patent

Patent achievement under the title "Pulse Shape Discrimination using Frequency Spectrum", which has a new approach of pulse shape discrimination using frequency spectrum, was pended in Germany.

9. Publications

Journals

1. H. I. Saleh A. H. Khalil, M. A. Ashour, and. A. E. Salama, "Novel Serial Parallel Multipliers", IEE Circuits, Devices, and systems, August 2001, Volume 148, Number 04, pp. 183-189.
2. M. A. Ashour and H. I. Saleh, "An FPGA Implementation Guide For Some Different Types of Serial-parallel Multiplier Structures", Microelectronics Journal, Volume 31, Number 3, March 2000.
3. Saleh, H., Engels, R.; Reinartz, R.; Reinhart, P.; Rongen, F. "A flexible compatible PCI interface for nuclear experiments", IEEE Transactions on Nuclear Science, Volume: 45 Issue: 3 Part: 1, June 1998, Page(s): 849 –851.
4. H. Saleh, M. Ashour, "A flexible PCI interface design for specific nuclear applications", A periodical of the Egyptian Society of Nuclear Sciences and Applications, Vol. 31, No. 2, pp. 317-323-99, 1998.
5. H. Saleh, R. Engels, R, Reinartz, P. Reinhart, F. Rongen, "A FLEXIBLE COMPATIBLE PCI INTERFACE FOR NUCLEAR EXPERIMENTS", IEEE Nuclear Science Symposium, New Mexico, Nov.1997.
6. H. Saleh and M. A. Ashour, "A Modified Design For The Two's Complement Fast Serial Parallel Multiplier", A periodical of the Egyptian Society of Nuclear Sciences and Applications, Vol. 29, No. 4, pp. 93-99, July 1996.

Presented Papers (Conferences)

7. Khalil, A.H.; Ashour, M.A.; Salama, A.E.; Saleh, H.I. "FPGA implemented fast two's complement serial-parallel multiplier with PCI interface", Proceedings of the Tenth International Conference on Microelectronics, ICM'98, Page(s): 21 –24.
8. Saleh, H., Zimmermann, E.; Brandenburg, G.; Halling, H. "Efficient FPGA-based multistage two-path decimation filter for noise thermometer", Microelectronics, 2001, ICM 2001 Proceedings, The 13th International Conference on, 2001 Page(s): 161 –164.
9. H. I. Saleh, and M. A. Ashour, "Configurable Multiplier/Divider Structure and Its FPGA-based Implementation", Al-Azhar Engineering Sixth International Conference, September 1-4, 2000.
10. *H. Saleh, E. Zimmermann, G. Brandenburg, and H. Halling, "Efficient FPGA-based Multistage Two-path Decimation Filter for Noise Thermometer*", proceedings of ISSPIT2001 Cairo.
11. *H. I. Saleh, M. A. Ashour, and A. E. Salama, "FPGA-based Efficient Two-path polyphase Multistage Half-band Decimated and Multi-band Filters*", Accepted in MELECON Conf., May 7^{th} –9^{th} , 2002, Cairo, Egypt.
12. Streun M.,Brandenburg G.,Larue H.,Saleh H.,Zimmermann E.,Ziemons K.,Halling H. **'Pulse shape discrimination of LSO and LuYAP scintillators for depth of interaction detection in PET', 2002 IEEE Medical Imaging Conference, 11.-16.11.2002, Norfolk, Virginia, USA. - (Conference record ; M11-15)**
13. *H. I. Saleh, M. A. Ashour, and A. E. Salama,* "GDFT TYPES MAPPING ALGORITHMS AND STRUCTURED REGULAR FPGA IMPLEMENTATION" SUBMITTED TO ISCAS 2003, THAILAND.

Submitted Papers

14. *H. I. Saleh, M. A. Ashour, and A. E. Salama,* "A Novel Pulse Shape Discrimination Approach", Submitted to IEEE Nuclear Transactions.